

PATENT APPLICATION
Docket No.: 9898-290
Client Ref. No.: SS-17735-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Geun-Hee CHO, et al.

Serial No.: 10/619,821 Examiner: Bocure, Tesfaldet

Filed: July 14, 2003 Art Unit: 2611

Confirmation No.: 7205

For: **DELAY LOCKED LOOP CIRCUIT FOR INTERNALLY CORRECTING DUTY CYCLE AND DUTY CYCLE CORRECTION METHOD THEREOF**

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

**APPLICANT'S COMMENTS ON EXAMINER'S STATEMENT OF REASONS FOR
ALLOWANCE**

Although the Examiner's statement attempts to narrow the patentable aspects to identified method limitations or circuit portions, patentability of the claimed invention should be considered as a whole. In addition, the Examiner refers to figures in the application, which describe a particular embodiment, not the claimed invention. And the invention is as defined in the claims, not as paraphrased by the Examiner. Finally, dependent claims that the Examiner did not refer to each include additional patentable limitations that further define the invention.

Applicant also points out that there is a typographical error in the identification of one of the references considered by the Examiner. JP 1081523 should have been identified as JP 1081526, as can be seen on the face of the reference and its associated English abstract, both of which are contained in this Image File Wrapper.

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Respectfully submitted,
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